UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 6,906,747 B2

Page 1 of 9

DATED

APPLICATION NO.: 09/913062 : June 14, 2005

INVENTOR(S)

: Sadami Okada

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

IN THE SPECIFICATION:

Column 2, line 32, change "solid" to --fixed--;

Column 6, line 5, delete current paragraph and insert therefor:

-- The CPU 19 outputs the raw data in which the gradation has been thus restored to a monitor display unit 22 and another signal processing system (S14 in Fig. 3).--;

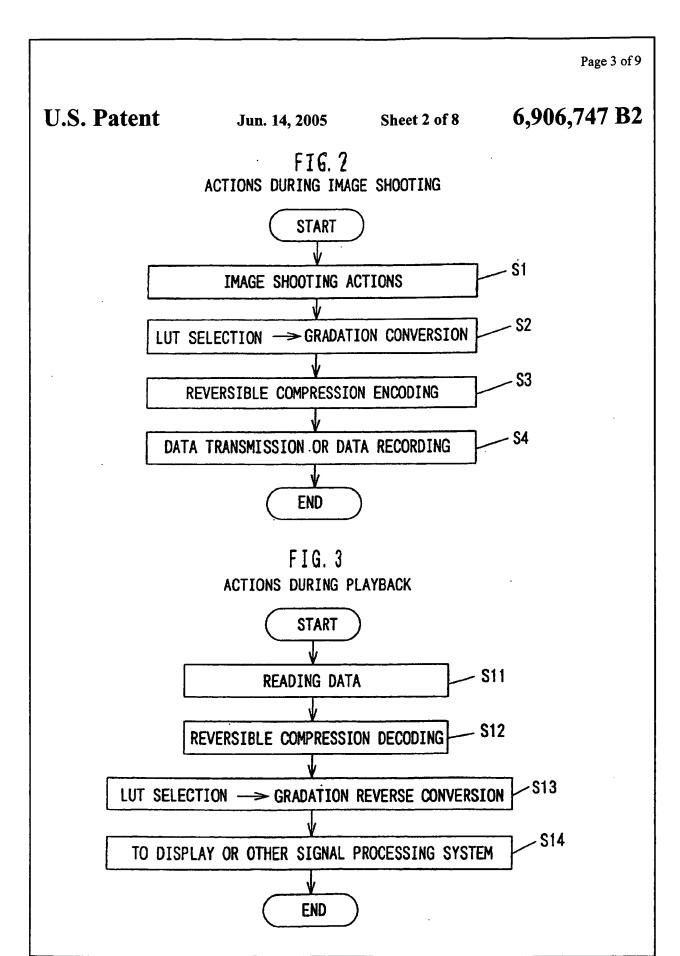
IN THE DRAWINGS:

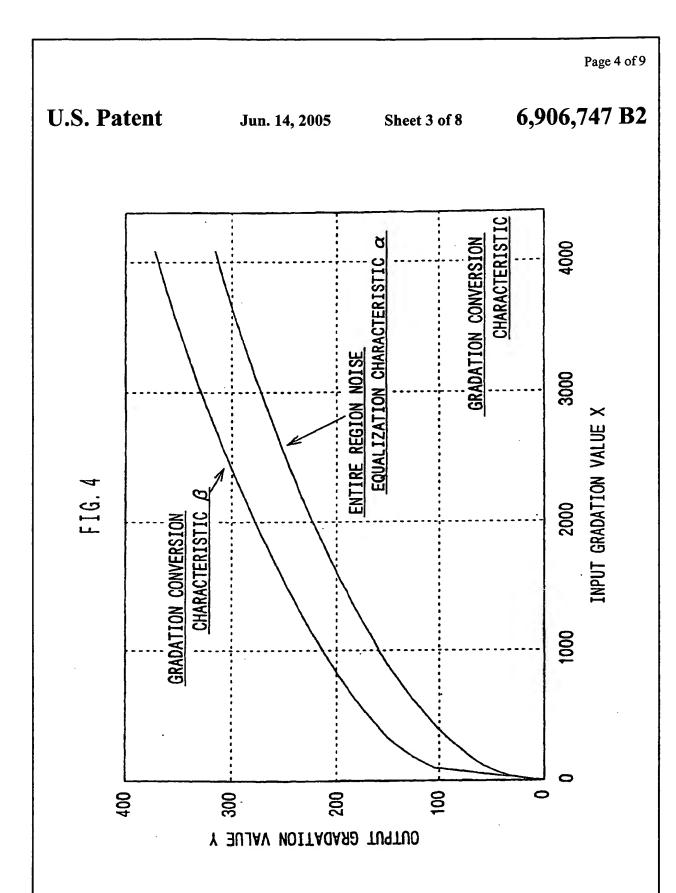
Please replace the drawings with the attached Replacement Formal Drawings. Figs. 1-8

Signed and Sealed this

Tenth Day of October, 2006

JON W. DUDAS Director of the United States Patent and Trademark Office





INPUT GRADATION VALUE X

